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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/801,455		
		Filing Date	March 16, 2004		
		First Named Inventor	Dajun YE		
		Art Unit	2816		
		Examiner Name	Tuan T. Lam		
Sheet	1	of	1	Attorney Docket Number	2085-01700

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
-n	AA	BERND-ULRICH H. KLEPSE, et al., "A 10G-HZ SiGe BiCMOS Phase-Locked-Loop Frequency Synthesizer", IEEE Journal on Solid State Circuits, Vol. 37, No. 3, March 2002, pp. 328-335.	
n	AB	M. ALIOTO AND G. PALUMBO, "Modeling and Optimized Design of Current Mode MUX/XOR and D-Flip-Flop", IEEE Trans. Of Circuits And Systems II: Analog And Digital Signal Processing, Vol. 47, No. 5, May 2000, pp. 452-461.	
n	AC	HAMID R. RATEGH, HIRAD SAMAVATI, THOMAS H. LEE, "A 5GHz, 32mW CMOS Frequency Synthesizer With An Injection-Locked Frequency Divider", IEEE Journal on Solid State Circuits, Vol. 35, No. 5, May 2000, pp. 113-116.	

Examiner Signature		Date Considered	4/14/2005
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